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STATEMENT BY APPLICANT**

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Sheet 2 of 2

Application Number	09/741,912
Filing Date	December 22, 2000
Applicants	Shuji MIYASAKA et al.
Group Art Unit	2644
Examiner Name	
Attorney Docket Number	0074/006001

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

EXAMINER INITIAL*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
AK		LEUNG, J. et al. "Performance Analysis of Automatic Assembly Systems with in-line Parallel Stations", IMA Journal of Mathematics Applied in Business and Industry, 1997, pp. 1-22, Vol. 8.	
AK		KURODA, I. et al. "Multimedia Processors", Proceedings of the IEEE, June 1998, pp. 1203-1221, Vol. 86, No. 6, IEEE, New York.	
AK		IWATA, E. et al. "Exploiting Coarse-Grain Parallelism in the MPEG-2 Algorithm", Technical Report TR-98-771, September 1998, pp. 1-13, Stanford University, Computer Systems LAB Retrieved from the Internet.	
AK		MESAROVIC, V. et al. "DTS Multichannel Audio Decoder on a 24-Bit Fixed-Point Dual-DSP Architecture" AES 106 th Convention Preprint, May 8, 1999, pp. 1-4, No. 4964	

EXAMINER

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